sequencer

Table of contents

PLC_1 [CPU 1214C DC/DC/DC]

PLC_1					
Project information					
Name	PLC_1	**Author**	togno	**Comment**	
Slot	1	**Rack**	0		
Catalog information					
Short designation	CPU 1214C DC/DC/DC	**Description**	Work memory 100 KB; 24VDC power supply with DI14 x 24VDC SINK/SOURCE, DQ10 x 24VDC and AI2 on board; 6 high-speed counters and 4 pulse outputs on-board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; PROFINET IO controller, I-device, transport protocol TCP/IP, secure Open User Communication, S7 communication, Web server, OPC UA: Server DA	**Article number**	6ES7 214-1AG40-0XB0
Firmware version	V4.5		False		

Connection resources

	Station resources - Reserved - Maximum	Station resources - Reserved - Configured	Station resources - Dynamic - Configured	Module resources - PLC_1 [CPU 1214C DC/DC/DC] - Configured
Maximum number of resources:		34	34	68
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	12	1	0	1
S7 communication:	8	0	0	0
Open user communication:	8	0	0	0
Web communication:	2	-	-	-
OPC UA client/server communication:	0	-	-	-
Other communication:	-	-	0	0
Total resources used:		1	0	1
Available resources:		33	34	67

Overview of addresses\Overview of addresses\Overview of addresses

Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
O	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
I	64	67	AI 2_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 33
O	1004	1005	Pulse_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 35

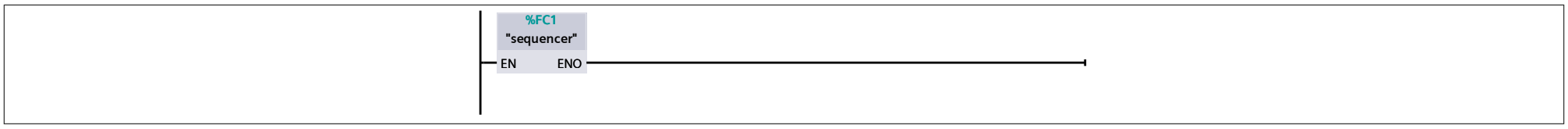
PLC_1 [CPU 1214C DC/DC/DC] / Program blocks

Main [OB1]

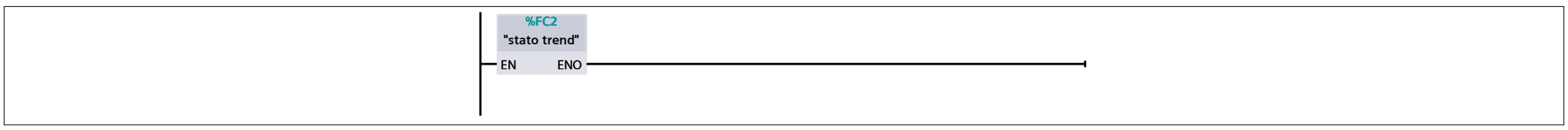
Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value
▼ Input		
Initial_Call	Bool	
Remanence	Bool	
Temp		
Constant		

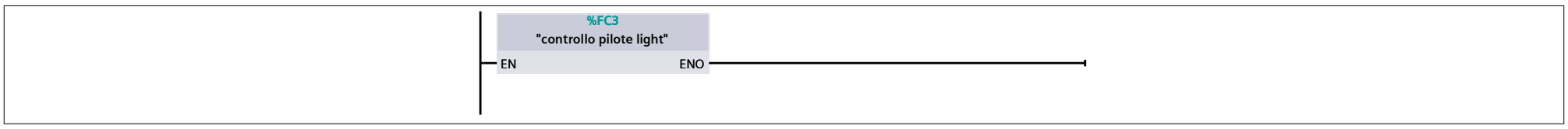
Network 1:



Network 2:



Network 3:



PLC_1 [CPU 1214C DC/DC/DC] / Program blocks

process variables [DB1]

process variables Properties							
General							
Name	process variables	**Number**	1	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Start value	Retain
▼ Static			
rampa	DInt	0	False
valvola 1	Int	0	False
valvola 2	Int	0	False
valvola 3	Int	0	False
valvola 4	Int	0	False
stato valvola ev1	Bool	false	False
stato valvola ev2	Bool	false	False
stato valvola ev3	Bool	false	False
stato valvola ev4	Bool	false	False

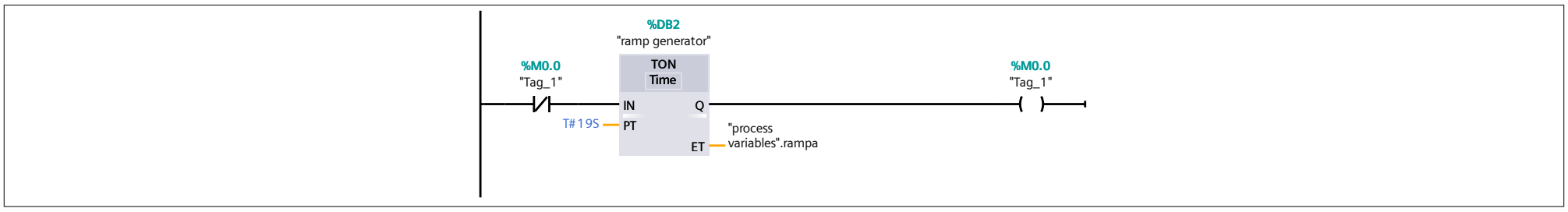
PLC_1 [CPU 1214C DC/DC/DC] / Program blocks

sequencer [FC1]

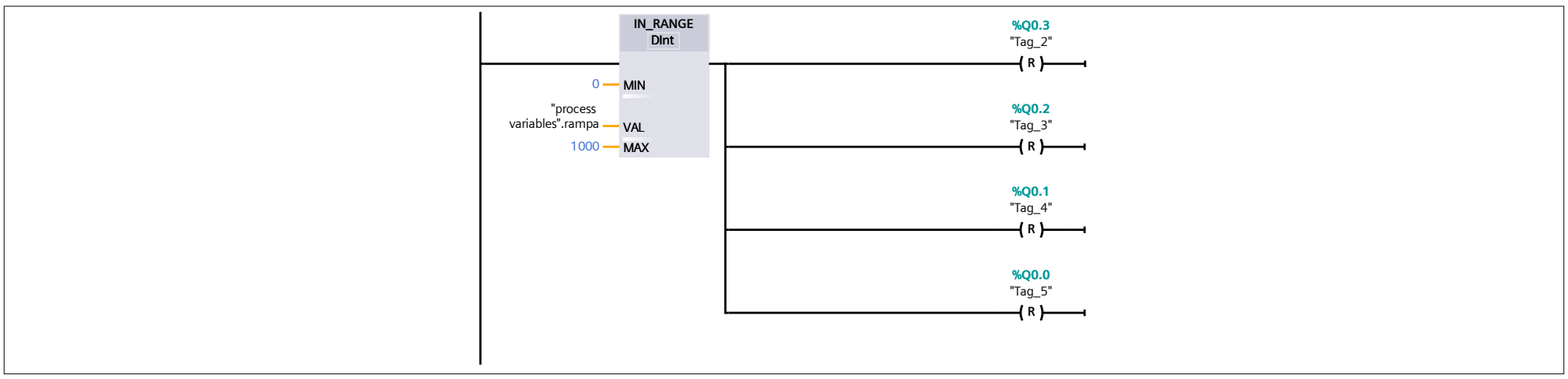
sequencer Properties							
General							
Name	sequencer	**Number**	1	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value
Input		
Output		
InOut		
Temp		
Constant		
▼ Return		
sequencer	Void	

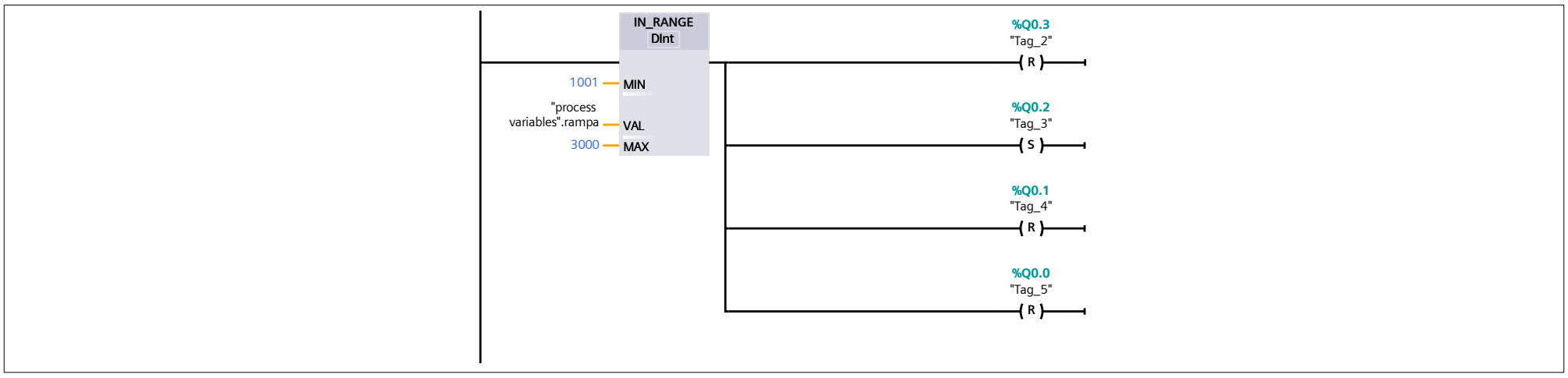
Network 1:



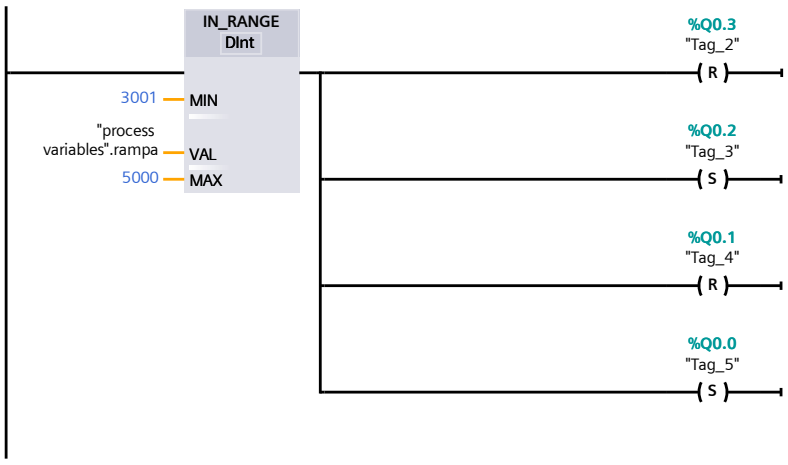
Network 2:



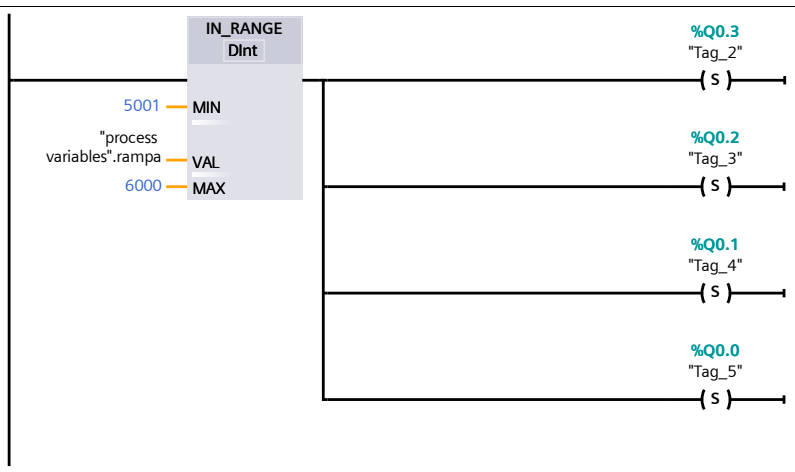
Network 3:



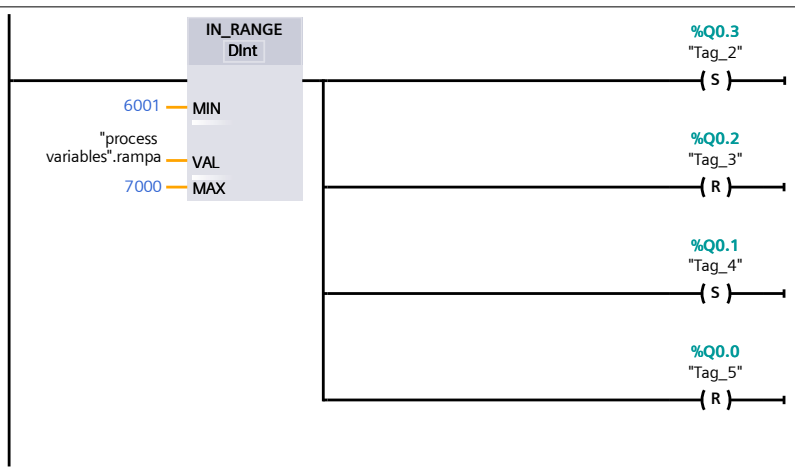
Network 4:



Network 5:

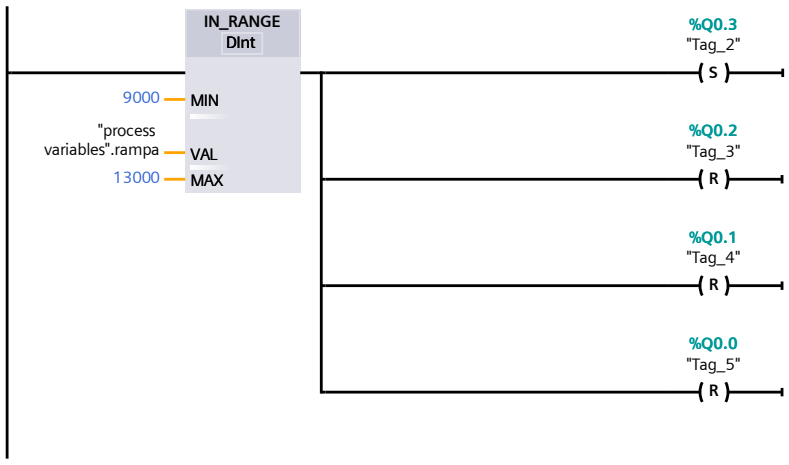


Network 6:

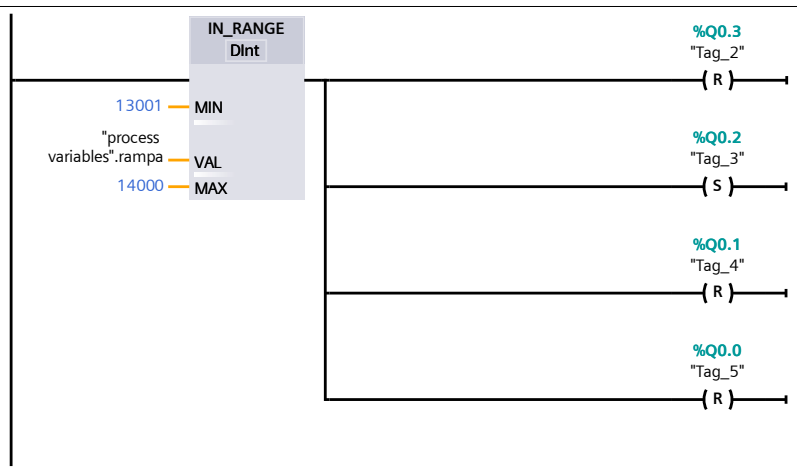


Network 7:

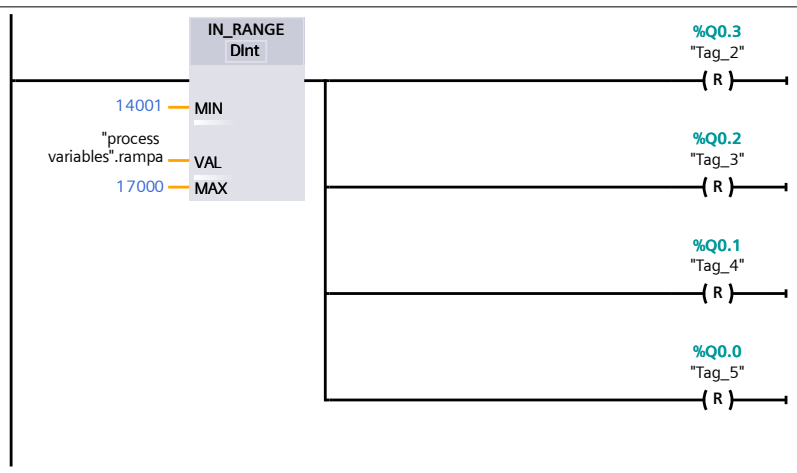
Network 8:



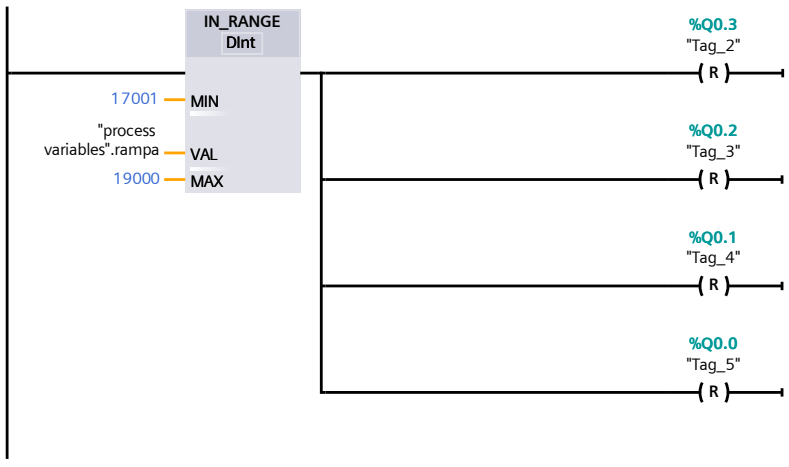
Network 9:



Network 10:



Network 11:



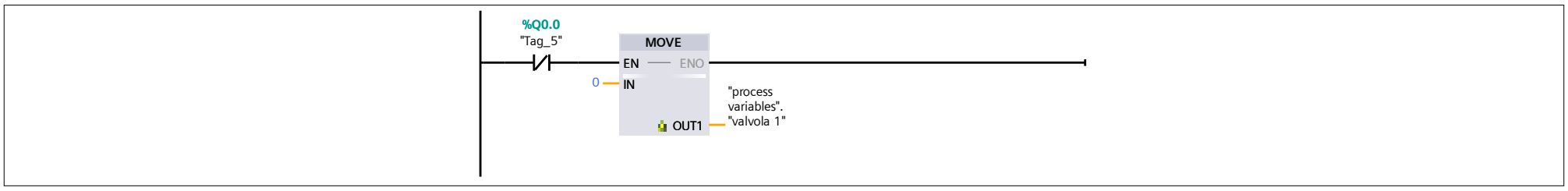
PLC_1 [CPU 1214C DC/DC/DC] / Program blocks

stato trend [FC2]

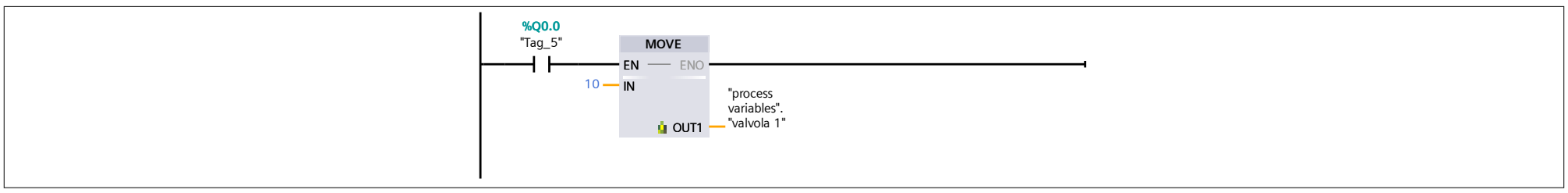
stato trend Properties							
General							
Name	stato trend	**Number**	2	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value
Input		
Output		
InOut		
Temp		
Constant		
▼ Return		
stato trend	Void	

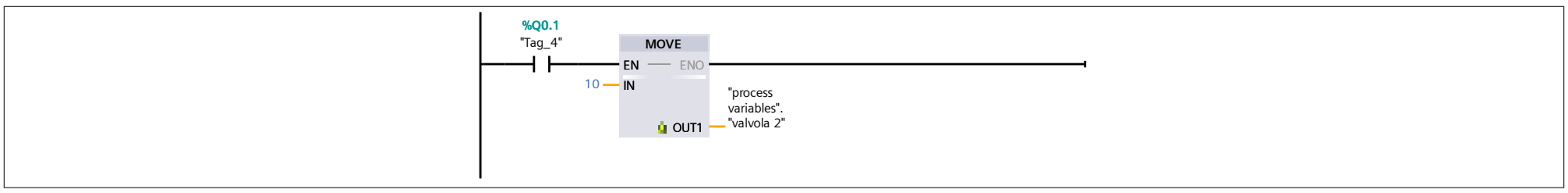
Network 1: controllo a 1 valvola 1



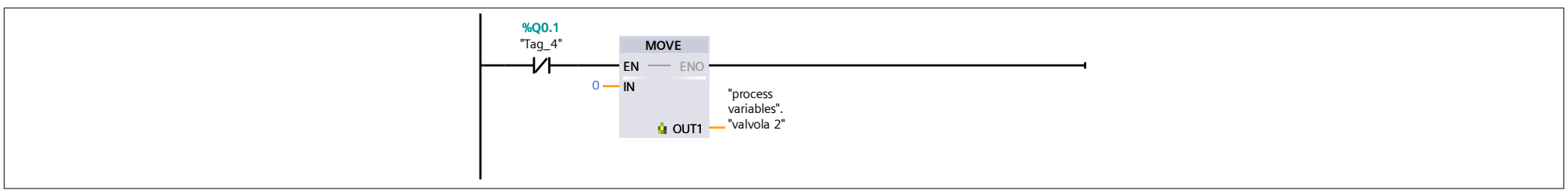
Network 2: controllo a 1 valvola 1



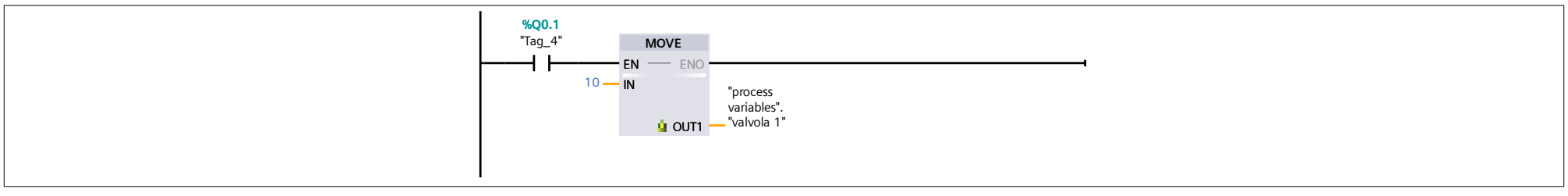
Network 3: controllo a 1 valvola 1



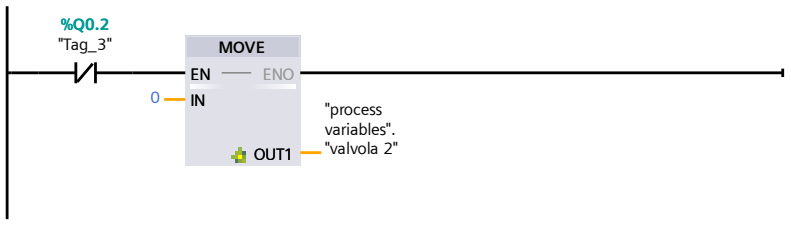
Network 4: controllo a 1 valvola 1



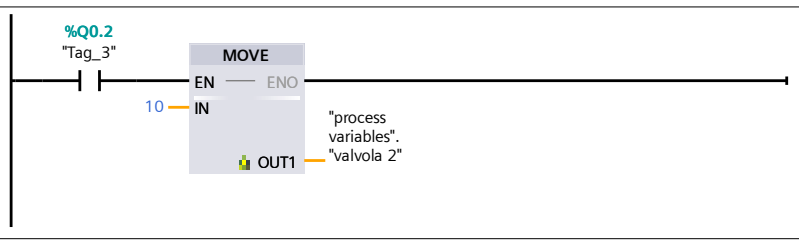
Network 5: controllo a 1 valvola 1



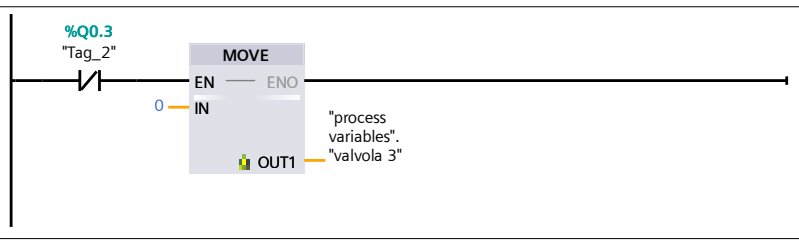
Network 6: controllo a 1 valvola 1



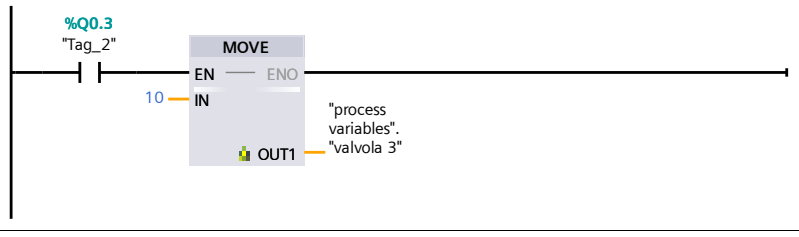
Network 7: controllo a 1 valvola 1



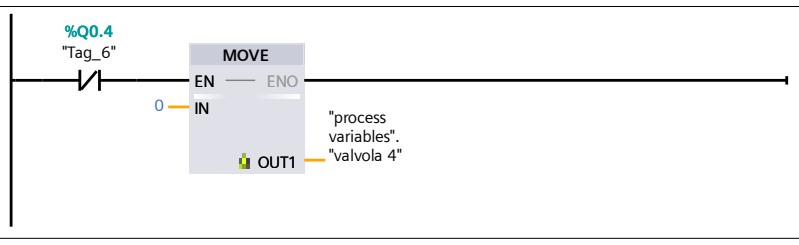
Network 8: controllo a 1 valvola 1



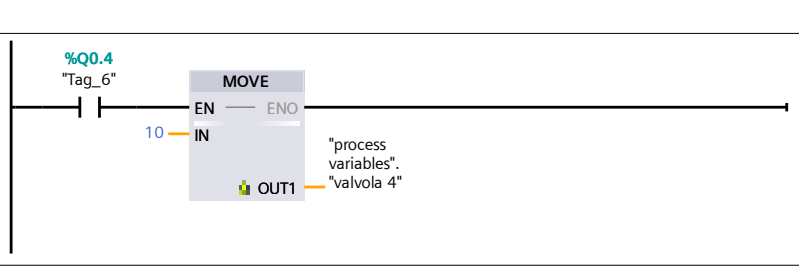
Network 9: controllo a 1 valvola 1



Network 10: controllo a 1 valvola 1



Network 11: controllo a 1 valvola 1



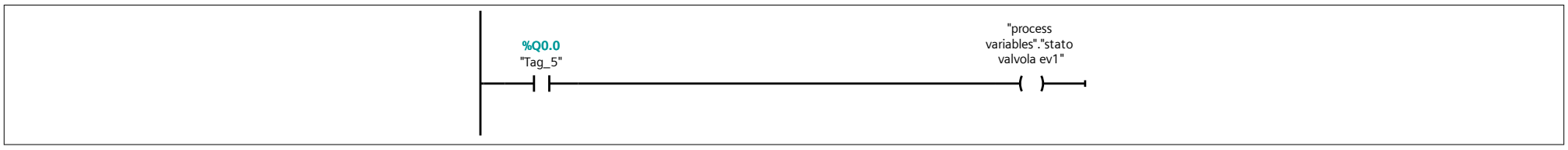
PLC_1 [CPU 1214C DC/DC/DC] / Program blocks

controllo pilote light [FC3]

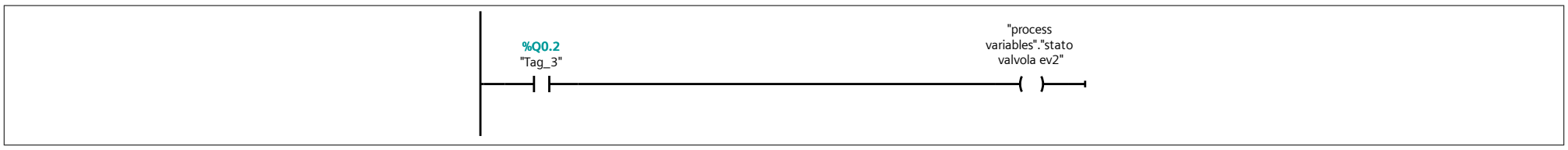
controllo pilote light Properties							
General							
Name	controllo pilote light	**Number**	3	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value
Input		
Output		
InOut		
Temp		
Constant		
▼ Return		
controllo pilote light	Void	

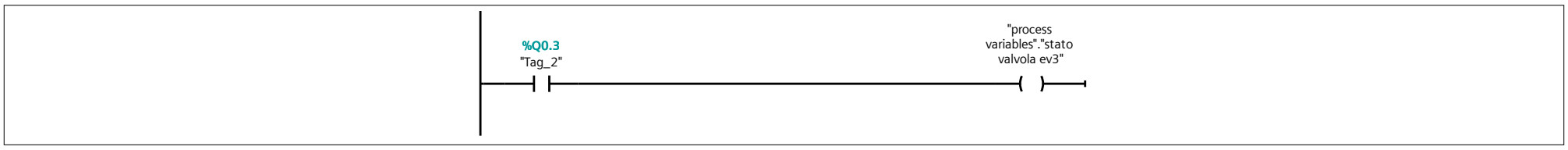
Network 1:



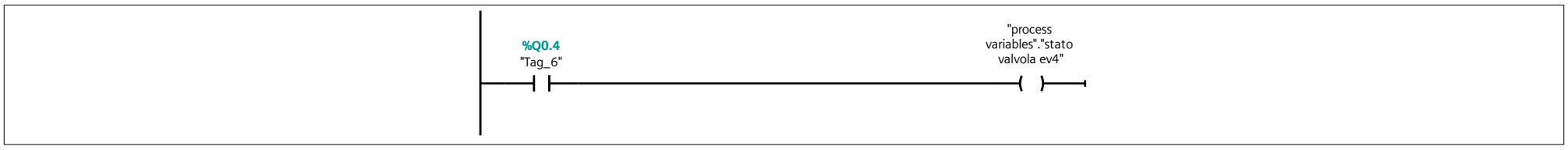
Network 2:



Network 3:



Network 4:



PLC_1 [CPU 1214C DC/DC/DC] / Program blocks / System blocks / Program resources

ramp generator [DB2]

ramp generator Properties							
General							
Name	ramp generator	**Number**	2	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**	Simatic	**Comment**		**Family**	IEC
Version	1.0	**User-defined ID**	IEC_TMR				

Name	Data type	Start value	Retain
▼ Static			
PT	Time	T#0ms	False
ET	Time	T#0ms	False
IN	Bool	false	False
Q	Bool	false	False

PLC_1 [CPU 1214C DC/DC/DC]

Technology objects

This folder is empty.

PLC_1 [CPU 1214C DC/DC/DC] / PLC tags / Default tag table [35]

PLC tags

PLC tags				
	Name	Data type	Address	Retain
	Tag_1	Bool	%M0.0	False
	Tag_2	Bool	%Q0.3	False
	Tag_3	Bool	%Q0.2	False
	Tag_4	Bool	%Q0.1	False
	Tag_5	Bool	%Q0.0	False
	Tag_6	Bool	%Q0.4	False

PLC_1 [CPU 1214C DC/DC/DC] / PLC tags / Default tag table [35]

User constants

User constants			
	Name	Data type	Value

PLC_1 [CPU 1214C DC/DC/DC] / PLC data types

System data types

This folder is empty.

PLC_1 [CPU 1214C DC/DC/DC] / Watch and force tables

Force table

Name	Address	Display format	Force value

PLC_1 [CPU 1214C DC/DC/DC]

Traces

Name

PLC_1 [CPU 1214C DC/DC/DC] / Traces

Measurements

This folder is empty.

PLC_1 [CPU 1214C DC/DC/DC] / Traces

Combined measurements

Name

PLC_1 [CPU 1214C DC/DC/DC] / OPC UA communication

Server interfaces

This folder is empty.

PLC_1 [CPU 1214C DC/DC/DC]

PLC alarm text lists

This folder is empty.

PLC_1 [CPU 1214C DC/DC/DC] / Local modules

PLC_1 [CPU 1214C DC/DC/DC]

PLC_1					
Project information					
Name	PLC_1	**Author**	togno	**Comment**	
Slot	1	**Rack**	0		
Catalog information					
Short designation	CPU 1214C DC/DC/DC	**Description**	Work memory 100 KB; 24VDC power supply with DI14 x 24VDC SINK/SOURCE, DQ10 x 24VDC and AI2 on board; 6 high-speed counters and 4 pulse outputs on-board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; PROFINET IO controller, I-device, transport protocol TCP/IP, secure Open User Communication, S7 communication, Web server, OPC UA: Server DA	**Article number**	6ES7 214-1AG40-0XB0
Firmware version	V4.5		False		

Connection resources

	Station resources - Reserved - Maximum	Station resources - Reserved - Configured	Station resources - Dynamic - Configured	Module resources - PLC_1 [CPU 1214C DC/DC/DC] - Configured
Maximum number of resources:		34	34	68
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	12	1	0	1
S7 communication:	8	0	0	0
Open user communication:	8	0	0	0
Web communication:	2	-	-	-
OPC UA client/server communication:	0	-	-	-
Other communication:	-	-	0	0
Total resources used:		1	0	1
Available resources:		33	34	67

Overview of addresses\Overview of addresses\Overview of addresses

Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
O	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
I	64	67	AI 2_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 33
O	1004	1005	Pulse_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 35