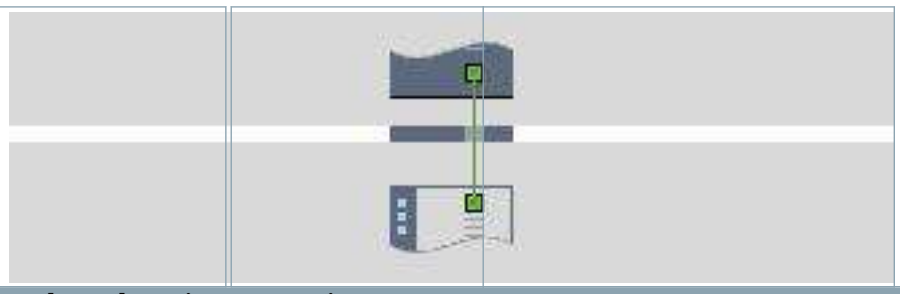
PLC_2 [CPU 1212C AC/DC/Rly]

PLC_2					
General\Project information					
Name	PLC_2	**Author**	Daniele	**Comment**	
Slot	1	**Rack**	0		
General\Catalog information					
Short designation	CPU 1212C AC/DC/Rly	**Description**	Work memory 50 KB; 120/240VAC power supply with DI8 x 24VDC SINK/SOURCE, DQ6 x relay and AI2 on board; 4 high-speed counters (expandable with digital signal board) and 4 pulse outputs on board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 2 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC-to-PLC communication	**Article number**	6ES7 212-1BE31-0XB0
Firmware version	V3.0				
PROFINET interface\General\Project information					
Name	PROFINET interface_1	**Comment**		**Name**	DI 8/DQ 6_1
Comment		**Name**	AI 2_1	**Comment**	
PROFINET interface\Ethernet addresses\Interface networked with					
Subnet:	PN/IE_1				
PROFINET interface\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.2	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_2
Converted name:	plcxb2d1ad	**Device number:**	0		
PROFINET interface\Digital inputs\Input filters					
I0.0 - I0.3	6.40ms	**I0.4 - I0.7**	6.40ms		
PROFINET interface\Digital inputs\Channel0					
Channel address	I0.0	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel1					
Channel address	I0.1	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel2					
Channel address	I0.2	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel3					
Channel address	I0.3	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel4					
Channel address	I0.4	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel5					
Channel address	I0.5	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel6					
Channel address	I0.6	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Digital inputs\Channel7					
Channel address	I0.7	**Enable rising edge detection**	0	**Enable falling edge detection**	0
Enable pulse catch	0				
PROFINET interface\Advanced options\Interface options					
Support device replacement without exchangeable medium	True	**Limit data infeed into the network**	True	**Use IEC V2.2 LLDP mode**	True
PROFINET interface\Advanced options\Anchor (ParameterRealtimeSettingsMenu)					
The TreeNode ParameterRealtimeSettingsMenu was not filled by some ACF					
PROFINET interface\Advanced options\Port [X1 P1]\General\Project information					
Name	Port_1	**Comment**			
PROFINET interface\Advanced options\Port [X1 P1]\Port interconnection\Local port:					
Local port:	PLC_2\PROFINET interface_1 [X1]\Port_1 [X1 P1]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface\Advanced options\Port [X1 P1]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface\Advanced options\Port [X1 P1]\Port options\Activate					
Activate this port for use	True				
PROFINET interface\Advanced options\Port [X1 P1]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface\Advanced options\Port [X1 P1]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface\Advanced options\Port [X1 P1]\Hardware identifier\Hardware identifier					
Hardware identifier	65				
PROFINET interface\Analog inputs\Noise reduction					
Integration time	50 Hz (20 ms)				
PROFINET interface\Analog inputs\Channel0					
Channel address	IW64	**Measurement type**	Voltage	**Voltage range**	0..10 V
Smoothing	Weak (4 cycles)			**Enable overflow diagnostics**	1
PROFINET interface\Analog inputs\Channel1					
Channel address	IW66	**Measurement type**	Voltage	**Voltage range**	0..10 V
Smoothing	Weak (4 cycles)			**Enable overflow diagnostics**	1
PROFINET interface\Digital outputs					
Reaction to CPU STOP	Use substitute value				
PROFINET interface\Digital outputs\Channel0					
Channel address	Q0.0	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Digital outputs\Channel1					
Channel address	Q0.1	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Digital outputs\Channel2					
Channel address	Q0.2	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Digital outputs\Channel3					
Channel address	Q0.3	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Digital outputs\Channel4					
Channel address	Q0.4	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Digital outputs\Channel5					
Channel address	Q0.5	**Substitute a value of 1 on a change from RUN to STOP.**	0		
PROFINET interface\Time synchronization					
Enable time synchronization via NTP server	Enable time synchronization via NTP server		IP addresses	**Server 1**	0.0.0.0
Server 2	0.0.0.0	**Server 3**	0.0.0.0	**Server 4**	0.0.0.0
Update interval	10sec				
PROFINET interface\Hardware identifier\Hardware identifier					
Hardware identifier	64	**Hardware identifier**	264		
PROFINET interface\I/O addresses\Input addresses					
Start address	0.0	**End address**	0.7	**Process image**	Cyclic PI
PROFINET interface\I/O addresses\Input addresses					
Start address	64	**End address**	67	**Process image**	Cyclic PI
PROFINET interface\I/O addresses\Output addresses					
Start address	0.0	**End address**	0.7	**Process image**	Cyclic PI
High speed counters (HSC)\HSC1\General\Enable					
Enable this high speed counter	0	**Enable this high speed counter**	0	**Enable this high speed counter**	0
Enable this high speed counter	0	**Enable this high speed counter**	0	**Enable this high speed counter**	0
High speed counters (HSC)\HSC1\General\Project information					
Name	HSC_1	**Comment**		**Name**	HSC_2
Comment		**Name**	HSC_3	**Comment**	
Name	HSC_4	**Comment**		**Name**	HSC_5
Comment		**Name**	HSC_6	**Comment**	
High speed counters (HSC)\HSC1\I/O addresses\Input addresses					
Start address	1000.0	**End address**	1003.7	**Start address**	1004.0
End address	1007.7	**Process image**	Cyclic PI	**Start address**	1008.0
End address	1011.7	**Process image**	Cyclic PI	**Start address**	1012.0
End address	1015.7	**Process image**	Cyclic PI	**Start address**	1016.0
End address	1019.7	**Process image**	Cyclic PI	**Start address**	1020.0

End address	1023.7	**Process image**	Cyclic PI	**Process image**	Cyclic PI
High speed counters (HSC)\HSC1\Hardware identifier\Hardware identifier					
Hardware identifier	257	**Hardware identifier**	258	**Hardware identifier**	259
Hardware identifier	260	**Hardware identifier**	261	**Hardware identifier**	262
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Enable					
Enable this pulse generator	0	**Enable this pulse generator**	0		
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information					
Name	Pulse_1	**Comment**		**Name**	Pulse_2
Comment					
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses					
Start address	1000.0	**End address**	1001.7	**Start address**	1002.0
End address	1003.7	**Process image**	Cyclic PI	**Process image**	Cyclic PI
Pulse generators (PTO/PWM)\PTO1/PWM1\Hardware identifier\Hardware identifier					
Hardware identifier	265	**Hardware identifier**	266		
Startup					
Startup after POWER ON	Warm restart - mode before POWER OFF	**Comparison preset to actual configuration**	Startup CPU even if mismatch	**Configuration time**	60000ms
Cycle					
Cycle monitoring time	150ms			**Enable minimum cycle time for cyclic OBs**	0
Minimum cycle time	1ms				
Communication load					
Cycle load due to communication	20%				
System and clock memory\System memory bits					
Enable the use of system memory byte	0	**Address of system memory byte (MBx)**	1	**First cycle**	
Diagnostic status changed		**Always 1 (high)**		**Always 0 (low)**	
System and clock memory\Clock memory bits					
Enable the use of clock memory byte	0	**Address of clock memory byte (MBx)**	0	**10 Hz clock**	
5 Hz clock		**2.5 Hz clock**		**2 Hz clock**	
1.25 Hz clock		**1 Hz clock**		**0.625 Hz clock**	
0.5 Hz clock					
Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	False		
Web server\Automatic update					
Enable automatic update	True	**Update interval**	0s		
Web server\User defined web pages					

Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	0	DI 8/DQ 6_1	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	1 Bytes	-	0	1 1
O	0	0	DI 8/DQ 6_1	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	1 Bytes	-	0	1 1
O	1000	1001	Pulse_1	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	2 Bytes	-	0	1 32
I	1012	1015	HSC_4	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 20
I	1004	1007	HSC_2	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 18
I	64	67	AI 2_1	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 16
I	1020	1023	HSC_6	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	4 Bytes	-	0	1 21
O	1004	1005	Pulse_3	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	2 Bytes	-	0	1 34
O	1002	1003	Pulse_2	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	2 Bytes	-	0	1 33
O	1006	1007	Pulse_4	-	PLC_2 [CPU 1212C AC/DC/Rly]	-	2 Bytes	-	0	1 35

Time of day\Local time					
Time zone	(UTC +01:00) Berlin, Bern, Brussels, Rome, Stockholm, Vienna				
Time of day\Daylight saving time					
Activate daylight saving time	0	**Difference between standard and daylight saving time**	60 mins		
Time of day\Daylight saving time\Start of daylight saving time					
Starting week of the month:	Last		Sunday	**of**	March
at	01:00 a.m.				
Time of day\Daylight saving time\Start of standard time					
	Last		Sunday	**of**	October
at	02:00 a.m.				
Protection					
Level of protection	No protection				
Protection\Password for read/write access					
Password		**Confirm password**			
Connection resources					
PG communication:	1	**OP communication:**	1	**S7 basic communication:**	0
S7 communication:	1	**Maximum number of S7 connection resources:**	32		
Anchor (AddressesOverviewMenu)					
The AddressesOverviewMenu was not filled by some ACF					

PLC_2 [CPU 1212C AC/DC/Rly] / Program blocks

Main [OB1]

Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Supervision	Comment
Temp				
Constant				

PLC_2 [CPU 1212C AC/DC/Rly] / Program blocks

db_orologio_riallineato [DB1]

db_orologio_riallineato Properties							
General							
Name	db_orologio_riallineato	**Number**	1	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
▼ Static										
▼ time_setup	DTL	0.0	DTL#1970-01-01-00:00:00	False	True	True	True	False		
YEAR	UInt	0.0	1970	False	True	True	True	False		
MONTH	USInt	2.0	1	False	True	True	True	False		
DAY	USInt	3.0	1	False	True	True	True	False		
WEEKDAY	USInt	4.0	5	False	True	True	True	False		
HOUR	USInt	5.0	0	False	True	True	True	False		
MINUTE	USInt	6.0	0	False	True	True	True	False		
SECOND	USInt	7.0	0	False	True	True	True	False		
NANOSECOND	UDInt	8.0	0	False	True	True	True	False		

PLC_2 [CPU 1212C AC/DC/Rly]

Technology objects

This folder is empty.

PLC_2 [CPU 1212C AC/DC/Rly] / PLC tags / Default tag table [16]

PLC tags

PLC tags									
	Name	Data type	Address	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Supervision	Comment

PLC_2 [CPU 1212C AC/DC/Rly] / PLC tags / Default tag table [16]

User constants

User constants				
	Name	Data type	Value	Comment

PLC_2 [CPU 1212C AC/DC/Rly]

PLC data types

This folder is empty.

PLC_2 [CPU 1212C AC/DC/Rly] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment

PLC_2 [CPU 1212C AC/DC/Rly]

PLC alarm text lists

This folder is empty.

PLC_2 [CPU 1212C AC/DC/Rly]

Local modules

This folder is empty.